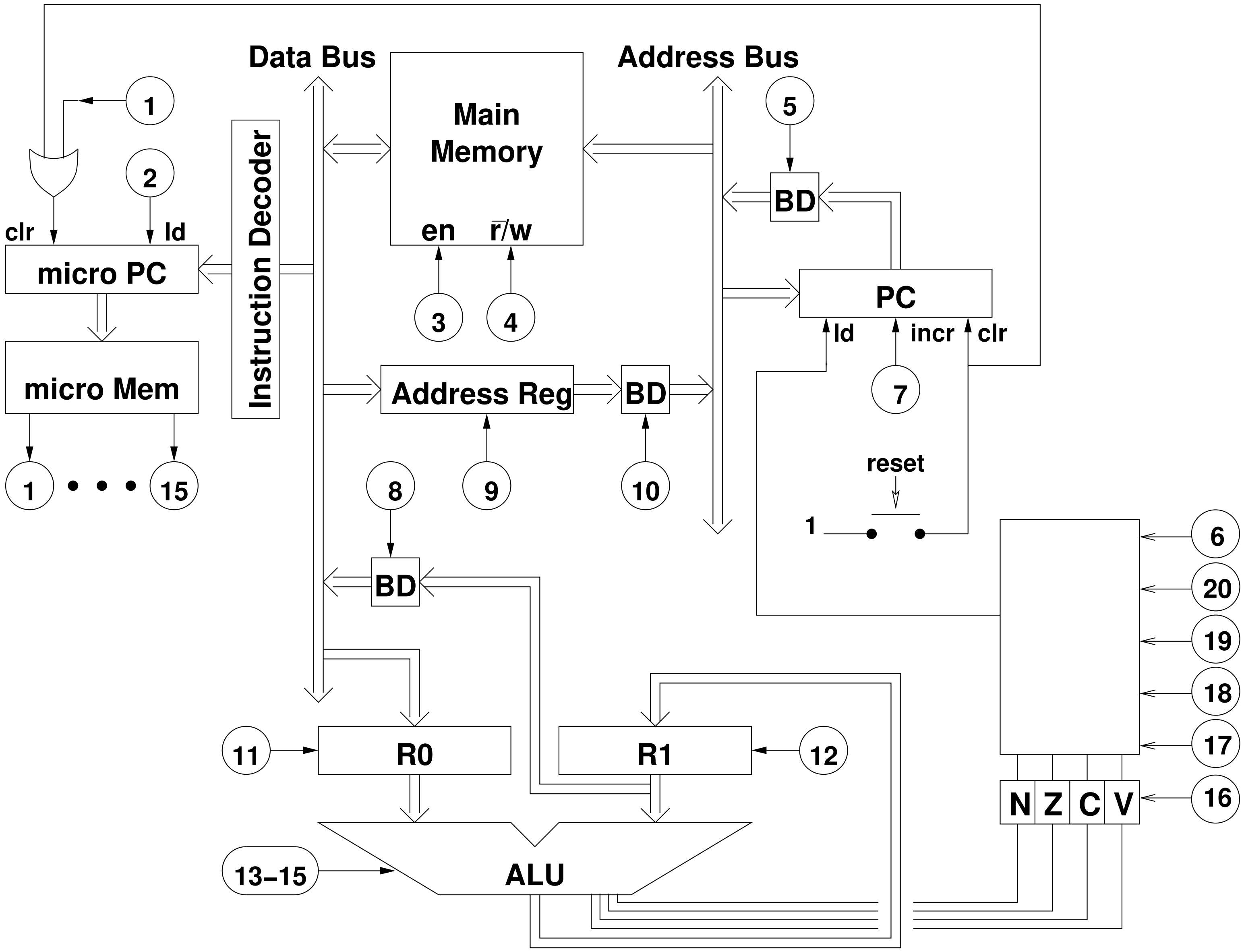
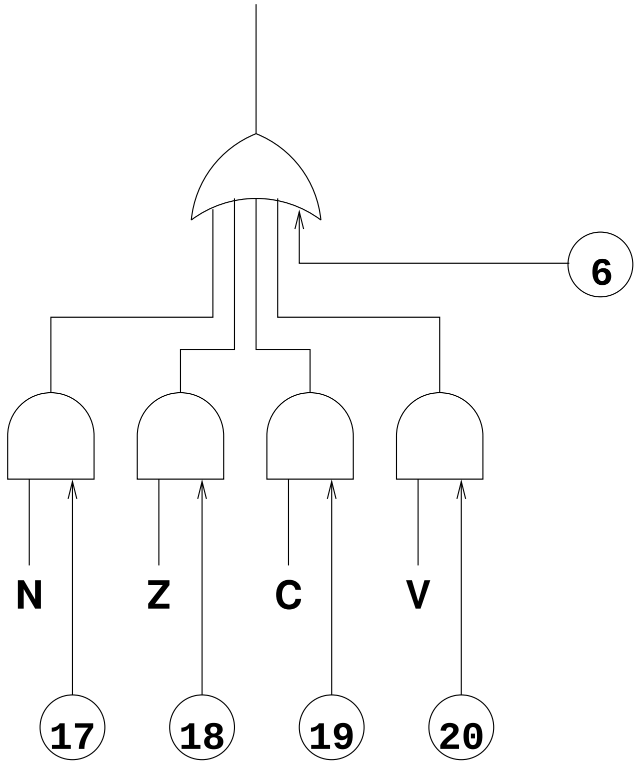
Sauts conditionnels



Contenu du circuit



Conteu de la micro mémoire et du décodeur d’instructions

micro mémoire

000000:	0 1 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0
000001:	1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
000010:	1 0 1 0 1 0 1 0 0 0 1 0 0 0 0 0 0 0 0 0
000011:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
000100:	1 0 1 0 0 0 0 0 0 1 1 0 0 0 0 0 0 0 0 0
000101:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
000110:	0 0 0 0 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
000111:	0 0 0 1 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
001000:	0 0 1 1 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
001001:	0 0 0 1 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
001010:	1 0 0 0 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
001011:	1 0 0 0 0 0 0 0 0 0 0 1 0 0 0 1 0 0 0 0
001100:	1 0 0 0 0 0 0 0 0 0 0 1 0 0 1 1 0 0 0 0
001101:	1 0 0 0 0 0 0 0 0 0 0 1 0 1 0 1 0 0 0 0
001110:	1 0 0 0 0 0 0 0 0 0 0 1 0 1 1 1 0 0 0 0
001111:	1 0 0 0 0 0 0 0 0 0 0 1 1 0 0 1 0 0 0 0
010000:	1 0 0 0 0 0 0 0 0 0 0 1 1 0 1 1 0 0 0 0
010001:	1 0 0 0 0 0 0 0 0 0 0 1 1 1 0 1 0 0 0 0
010010:	1 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 0 0 0 0
010011:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
010100:	1 0 0 0 0 1 0 0 0 1 0 0 0 0 0 0 0 0 0 0
010101:	
...	

décodeur d’instructions

00000:	000001	NOP
00001:	000010	LDIMM
00010:	000011	LD
00011:	000101	ST
00100:	001011	COPY
00101:	001100	SHL
00110:	001101	SHR
00111:	001110	ADD
01000:	001111	SUB
01001:	010000	AND
01010:	010001	OR
01011:	010010	NOT
01100:	010011	JAL
01101:		
01110:		
01111:		
10000:		
10001:		
10010:		
10011:		
10100:		
10101:		
...		

Conteu de la micro mémoire et du décodeur d'instructions

micro mémoire

001010:	1 0 0 0 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0
001011:	1 0 0 0 0 0 0 0 0 0 0 1 0 0 0 1 0 0 0 0
001100:	1 0 0 0 0 0 0 0 0 0 0 1 0 0 1 1 0 0 0 0
001101:	1 0 0 0 0 0 0 0 0 0 0 1 0 1 0 1 0 0 0 0
001110:	1 0 0 0 0 0 0 0 0 0 0 1 0 1 1 1 0 0 0 0
001111:	1 0 0 0 0 0 0 0 0 0 0 1 1 0 0 1 0 0 0 0
010000:	1 0 0 0 0 0 0 0 0 0 0 1 1 0 1 1 0 0 0 0
010001:	1 0 0 0 0 0 0 0 0 0 0 1 1 1 0 1 0 0 0 0
010010:	1 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 0 0 0 0
010011:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
010100:	1 0 0 0 0 1 0 0 0 1 0 0 0 0 0 0 0 0 0 0
010101:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
010110:	1 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 1 0 0 0
010111:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
011000:	1 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 1 0 0
011001:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
011010:	1 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 1 0
011011:	0 0 1 0 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0
011100:	1 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 1
...	

décodeur d'instructions

00000:	000001	NOP
00001:	000010	LDIMM
00010:	000011	LD
00011:	000101	ST
00100:	001011	COPY
00101:	001100	SHL
00110:	001101	SHR
00111:	001110	ADD
01000:	001111	SUB
01001:	010000	AND
01010:	010001	OR
01011:	010010	NOT
01100:	010011	JAL
01101:	010101	JN
01110:	010111	JZ
01111:	011001	JV
10000:	011011	JC
10001:		
10010:		
10011:		
10100:		
...		